

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) : Robert P. Loce

TITLE : METHODS AND APPARATUS FOR ANTIALIASING
USING SELECTIVE IMPLEMENTATION OF LOGICAL
AND AVERAGING FILTER OPERATIONS

APPLICATION NO. : 10/601,686

FILED : June 23, 2003

CONFIRMATION NO. : 7702

EXAMINER : ZHENG, Jacky X

ART UNIT : 2625

ALLOWED : September 21, 2007

ATTORNEY DOCKET : D/A3092
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RESPONSE TO STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Mail Stop Issue Fee

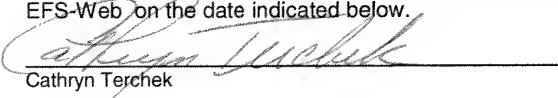
Dear Sir:

The Examiner indicated that claims 1-18 are allowed over the prior art. This recognition of allowable subject matter is acknowledged and appreciated.

However, while the applicants agree that the claims are allowable, the applicants do not concede that the claims are only allowable for the precise reasons stated by the Examiner.

CERTIFICATE OF ELECTRONIC TRANSMISSION

I hereby certify that this paper and/or fee is being transmitted to the USPTO by electronic transmission via EFS-Web on the date indicated below.


Cathryn Terchek

Date:  October 5 2007

Furthermore, the applicants do not concede that the Examiner's statements are entirely accurate and precise. For example, with respect to the comments of the Examiner regarding "selecting between different types of filters", logical and averaging filters are examples. For instance, some embodiments might select between a logical filter and some other kind of non-logical filter.

Respectfully submitted,

Fay Sharpe LLP

October 5, 2007
Date

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